

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Hiroshi MORIOKA**

Serial Number: **Not Yet Assigned**

Filed: **October 27, 2003**

Customer No.: 38834

For: **MICRO PATTERN FORMING METHOD AND SEMICONDUCTOR DEVICE
MANUFACTURING METHOD**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

October 27, 2003

Sir:

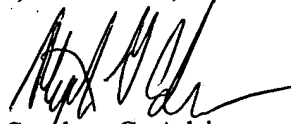
In compliance with 37 CFR 1.56, Applicant calls to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith, except for the U.S. reference.

In the event there are any fees due in connection with the filing of this paper, please charge

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Respectfully submitted,
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Enclosures: PTO-1449; References(4)

**INFORMATION
DISCLOSURE
STATEMENT
PTO-1449**

Atty. Docket No. 032045

Serial No. New Appln.

Applicant(s): Hiroshi MORIOKA

Filing Date: October 27, 2003

Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial		Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	6,187,688	Ohkuni et al.	02/13/01			
_____	AB						
_____	AC						
_____	AD						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Translation (Yes or No)
_____	AE	10-98029	04/14/98	Japan	Abstract. Cited in the specification.
_____	AF	2928391	05/14/99	Japan	No. Cited in the specification.
_____	AG	2971443	08/27/99	Japan	Abstract. Cited in the specification.
_____	AH	2001-196355	07/19/01	Japan	Abstract. Cited in the specification.
_____	AI				

OTHER DOCUMENTS

_____	AJ	
_____	AK	
Examiner	Date Considered	